

3A. ABSTRACT OF RESEARCH PROPOSAL

We propose to develop integrated circuits fabricated in wide-bandgap semiconductor technology for unconventional fields, including monolithic switchmode power conversion and precision diathermy. This project is very timely.[1]–[3] Foundry access to Field-Effect Transistors (FETs) made in the Gallium-Nitride (GaN) system in Monolithic Microwave Integrated Circuits (MMICs) will first become commercially available circa 2008, and is already available to contributing researchers. Only discrete FETs have been available previously.

Large R&D concerns (University of California, Rockwell Science, Triquint, etc.) concentrate on applications of GaN in mobile communications and radar, to the exclusion of niche markets, and to the exclusion of markets with no existing semiconductor penetration.[4, 5] In other words, every wide-bandgap company sees wide-bandgap as an upward step in applications currently lucratively served by an existing Silicon or III-V technology.[6, 7] In contrast, we see wide-bandgap as a technology that can take transistors where they have never been before—namely miniaturised high-voltage applications (e.g., monolithic switchmode power conversion), harsh RF environments (e.g., localised microwave diathermy in tissue), and extremely wide dynamic range circuits (e.g., ultralinear amplification, low phase-noise oscillators, vacuum-tube replacement). Entry into such novel fields will demand ab initio circuit design. We will prove or disprove the feasibility and identify benefits of bringing GaN devices to these fields. We believe this is being overlooked world-wide, because it is too risky and too novel to attract commercial funding.

Both investigators have 30+ years of experience in electronic design and both have recent MMIC and GaN involvement. We have the skills and capability to design MMICs and guide postgraduate students and emerging researchers to expand GaN’s sphere of application.

Wide-bandgap semiconductor active devices in the Gallium-Nitride material system have three acknowledged advantages over Silicon and III-V technologies: (1) They have 10 times higher power-bandwidth than even Indium-Phosphide; (2) they have intrinsic high-voltage capability; and (3) they can operate at higher temperatures and in harsh environments.

Our vision for GaN-based switchmode systems is to integrate the entire converter in an IC using GaN to implement resonant converters at GHz frequencies.[16, 17] The last 20 years has seen SwitchMode Power Supplies (SMPS) shrink in cost and size as operating frequency increased. However, there has never been a demonstration of *wholly monolithic SMPS*, since silicon and GaAs MMICs are not up to the task. MicroGaN, a foundry spinoff at the University of Ulm in Germany, have a process with $F_t > 30\text{GHz}$ and a breakdown voltage of 70V, sufficient for converters operating between 42V and 12V. This range, for example, will be in great demand once domestic automobiles convert to the new standard of 42V.

The vision for GaN-based diathermy tools is to replace the current “microwave antenna on a length of thin coax” [19, 20] with a monolithic dc-microwave oscillator based on a GaN MMIC. The power will be supplied to the probe tip at dc, giving low loss, low cost, and fine mechanical form. Freed from the need to deliver power through coax, the RF energy can be delivered at tens of GHz, giving highly localised heat.

The vision for linear amplifiers and low phase-noise oscillators rests on the expectation that the high-voltage capability of GaN will give it a wider dynamic range than any previous solid-state technology.[10, 13, 8] We will prove or disprove our expectation by design and comparison of circuits for *low-power* application, including vacuum-tube replacement, [12] and low phase-noise oscillators employing high-voltage dielectric resonators [21].

Our plan is the traditional “fabless design” model, namely to fit CAD models to transistors and passives from potential foundries, design circuits, have them fabricated, test and analyse the returned chips, and publish our results. We will be able to test all of our novel ideas in parallel, since multiple independent circuits may be fabricated in each run, using a “pizza” mask.

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